



PECL Clock Oscillators, SMD Packages

MODEL	C71
FREQ' RANGE	100.00 to 166.00 MHz
FREQ' STABILITY	Standard: ± 25 PPM, ± 50 PPM Parameters included: Operating temp. Range, Calibration at +25°C, Supply & Load Variations, Aging
OPERATING TEMP' RANGE	See "How - To - Order" instructions
OUTPUT WAVEFORM	PECL High Level (VOH): 0.9Vcc Min. Low Level (VOL): 0.1Vcc Max. Symmetry: (50 \pm 10%) or (50 \pm 5%) Rise/ fall time: 5nSec Yyp.
LOAD	15 pF Typ
SUPPLY VOLTAGE	3.3 Vdc , 5.0 Vdc
SUPPLY CURRENT	25 mA Max. for F < 25.00 MHz 35 mA Max. for frequencies between 25.00 MHz to 50.00 45 mA Max. for F > 50.00 MHz
ENABLE / DISABLE	Enable: High (above 2.2V) or open Disable (High Impedance): Low (below 0.8V)
SMD PACKAGE	7.0 x 5.0 x 1.7 mm. 6 Pads

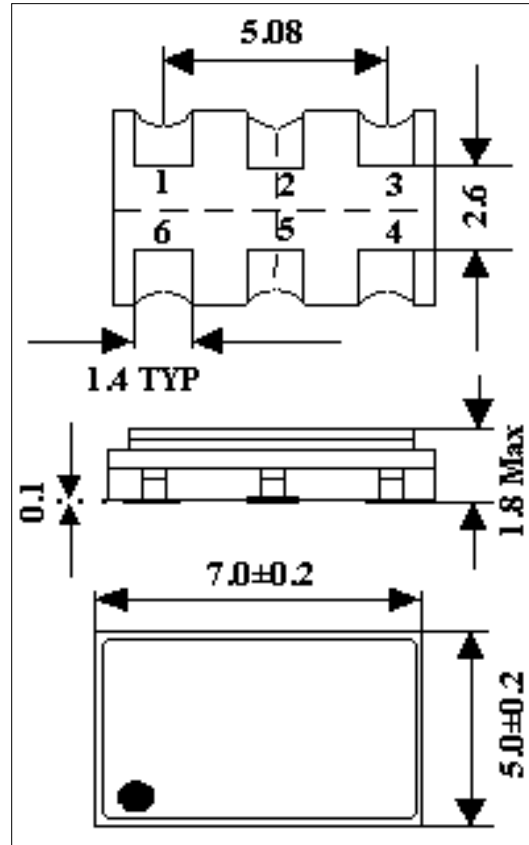
Environmental Conditions	
SHOCK	IEC 68-2-27 (Test Ea) 30G, 18 mSec, Half Sine
VIBRATION	IEC 68-2-6 (Test Fc), 0.35mm, 5G, 10-2000Hz, 6 cycles/ axis (Fig.2, Table V)
THERMAL SHOCK	IEC 68-2-14 (Test Na), 30 Min. in each extreme temperature
SOLDERING HEAT	235°C \pm 5°C for 10 Sec. Max. Solder melting point: 217°C



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Pin	Function
1	TR-STATE
2	N/C
3	GND
4	Output
5	Output ~
6	Vcc

Dimensions in mm.



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