

## LVDS CRYSTAL OSCILLATOR IN CERAMIC LCC PACKAGE - XO75LVD1 Series

### FEATURES

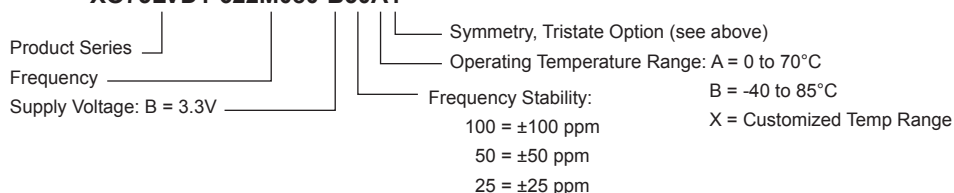
- RoHS Compliant (Pb-Free), LVDS Compatible Signals
- Inherent Low Power and Low EMI Emission
- Low Phase Jitter with New Generation PLL Design
- Complimentary Output, Tri-state Enable/Disable Standard, 7x5x2 mm SMD package

### SPECIFICATIONS

<b>Frequency Range</b>	300 MHz to 700 MHz
<b>Standard Frequency</b>	312.500 MHz, 622.080 MHz
<b>Input Voltage (Vcc)</b>	B = +3.3 VDC $\pm$ 5%
<b>Input Current</b>	80 mA Maximum
<b>Storage Temperature</b>	-55°C to 125°C
<b>Overall Frequency Stability</b>	100 = $\pm$ 100 ppm; 50 = $\pm$ 50 ppm; 25 = $\pm$ 25 ppm
<b>Temperature Range</b>	A = 0°C to 70°C; B = -40°C to 85°C
<b>Standard Stability</b>	50A = $\pm$ 50 ppm / 0°C to 70°C
<b>Duty Cycle</b>	3 = Tristate 55/45% symmetry
<b>Output Load</b>	100 Ohms across differential outputs (Offset 1.25V Typ)
<b>Logic "1" / Logic "0" Level</b>	1.43V Typ / 1.10V Typ
<b>Rise/Fall Time (Tr/Tf)</b>	1.0 ns Maximum at 20% to 80% Vp-p
<b>Start-up time</b>	5 ms Maximum
<b>Phase Jitter (RMS, 1 Sigma)</b>	1.6 ps Typical for fj = 12KHz to 20MHz, at 622.080 MHz
<b>Tristate Function</b>	Input (Pin 1) High (> 2.2V) or open: Output (Pin 4, 5) active Input (Pin 1) Low (< 0.4V): Output disabled in high impedance
<b>Enable/Disable Time</b>	100 ns Maximum

### Creating a Part Number

#### XO75LVD1-622M080-B50A1



### OUTLINE DRAWING

