OKI Semiconductor

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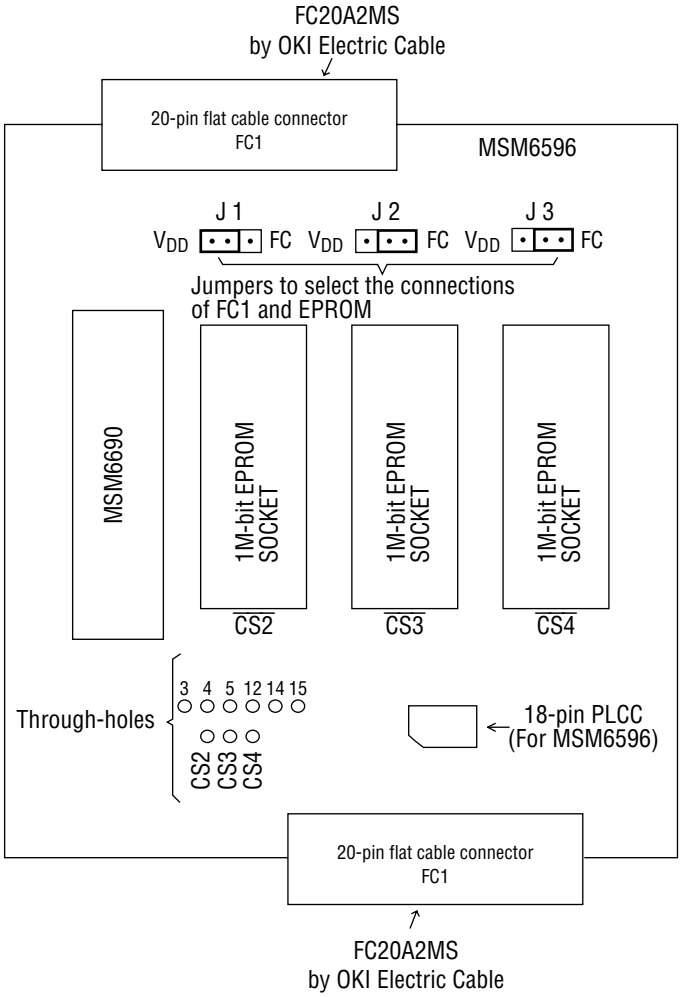
Previous version: May 1997

MSM6596 DEMO BOARD

MSM6596 Demonstration Board

BOARD DESIGN



HOW TO USE THE BOARD

1. EPROM Connection Procedure
   - Position to insert the EPROM
     When one serial register is used, set the EPROMs at the positions of $\overline{CS2}$ and $\overline{CS3}$ or the positions of $\overline{CS3}$ and $\overline{CS4}$.

   - Jumper setting
     When the EPROMs are set at the $\overline{CS2}$ and $\overline{CS3}$, set J1 and J2 of jumpers to the right side (FC side) and J3 of jumper to the left side (V_{DD} side).
     When the EPROMs are set at the $\overline{CS3}$ and $\overline{CS4}$, set J2 and J3 of jumpers to the right side (FC side) and J1 of jumper to the left side (V_{DD} side).

2. 18-pin PLCC (for MSM6596) Connection Procedure

   - 18-pin PLCC
     Mount the MSM6596 of the 18-bit PLCC

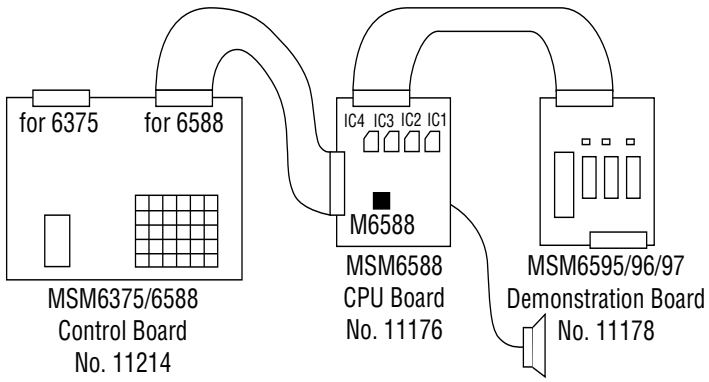
   - Jumper setting
     In the evaluation of EPROM, when EPROMs are set at the positions of $\overline{CS2}$ and $\overline{CS3}$, connect the through-holes $\overline{CS2}$ and 4, and $\overline{CS3}$ and 3 with the jumper wire.
     When EPROMs are at the positions of $\overline{CS3}$ and $\overline{CS4}$, connect the through-holes $\overline{CS3}$ and 4, and $\overline{CS4}$ and 3 with jumper wire.

3. MSM6588 Demonstration Board Connection Procedure

   - To play the voice of EPROM, connect the 20-pin flag cable connector FC1 to the connector for the MSM6596 of the MSM6588 demonstration board.

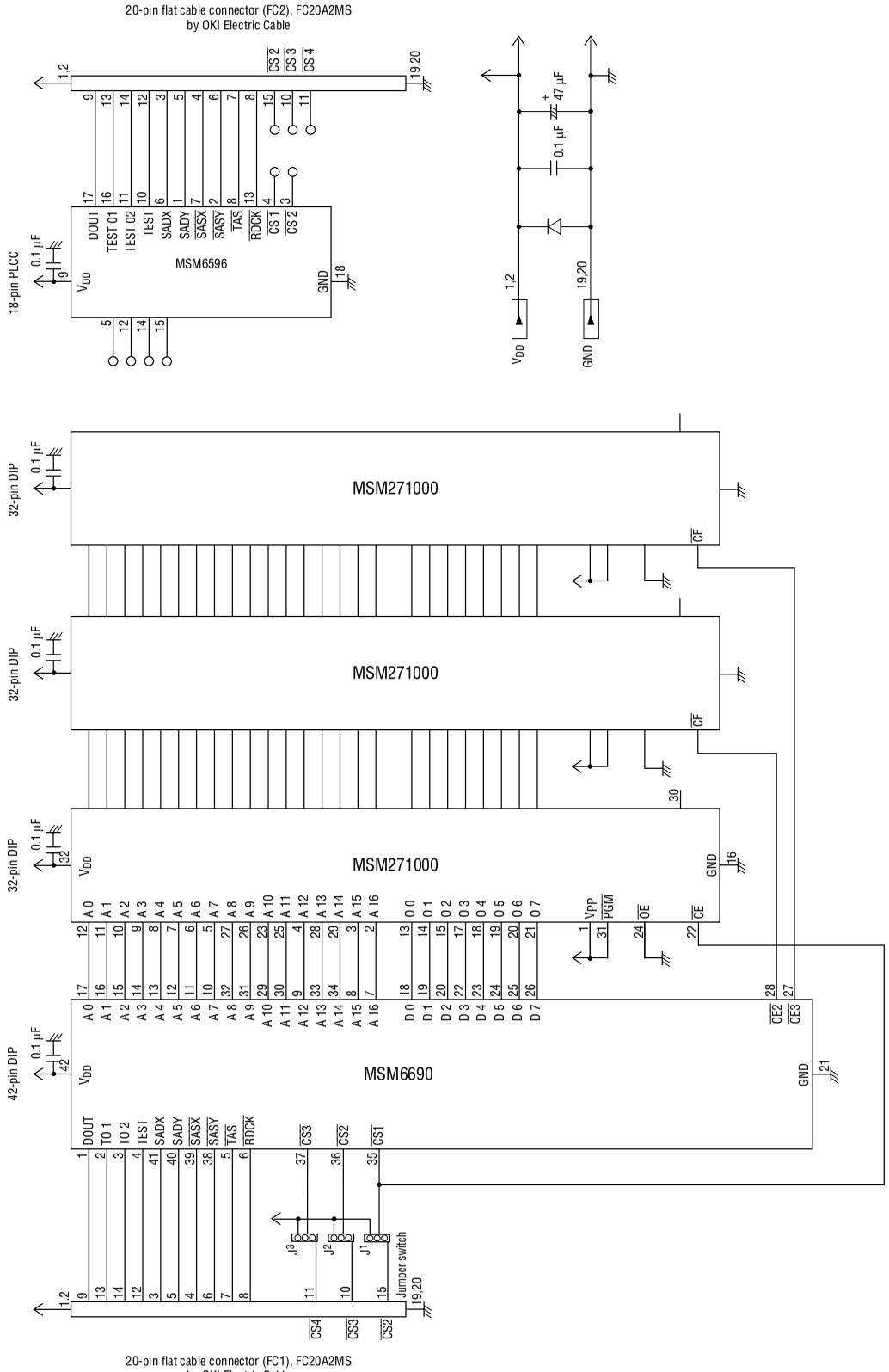
   - To play the voice of 18-pin PLCC, connect the 20-pin flat cable connector FC2 to the connector for the MSM6596 of the MSM6588 demonstration board.

   <Connecting Diagram>



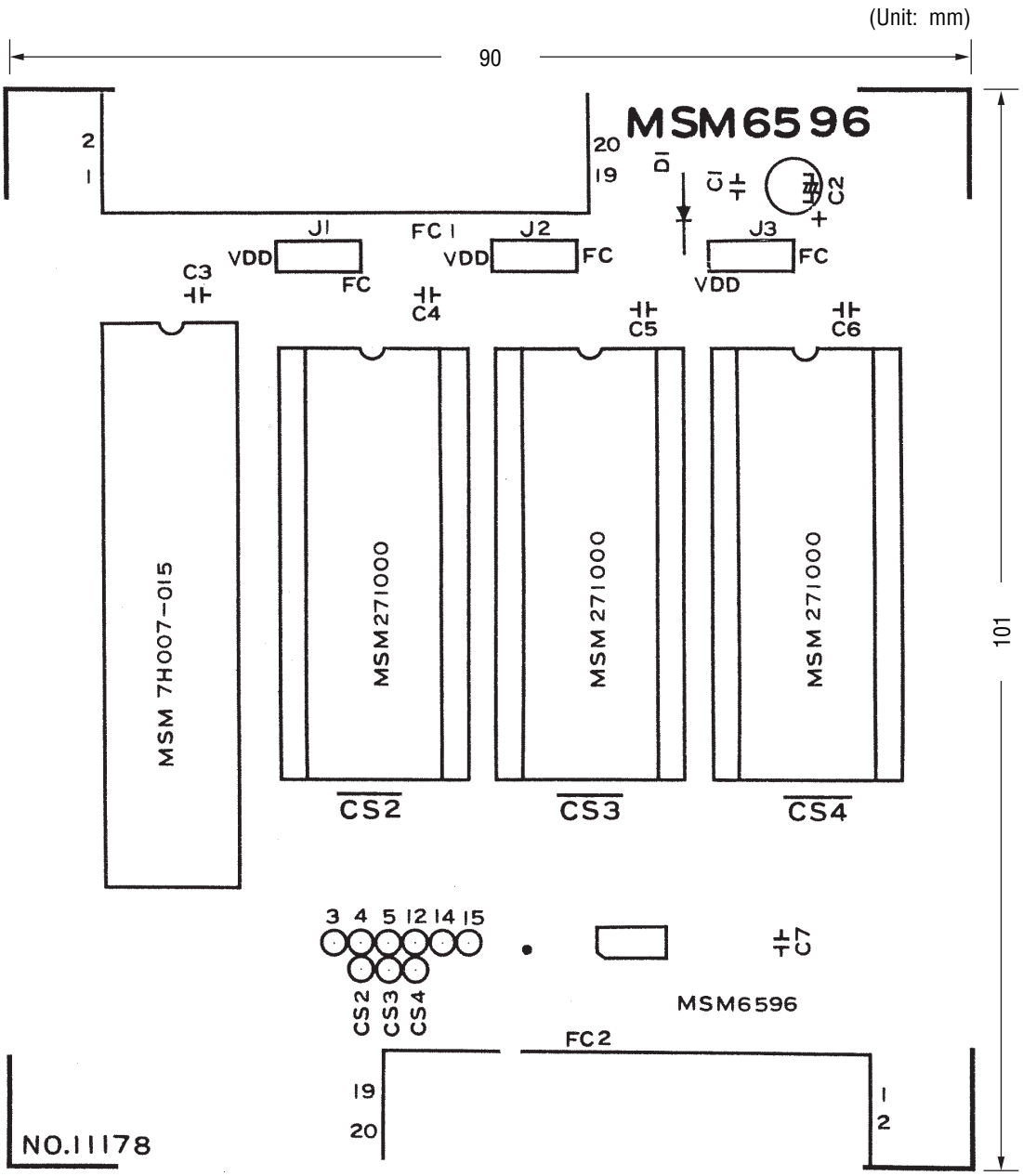
Note: When the serial register is not mounted to IC1 on the MSM6588 CPU board (No. 11176), the circuit does not operate normally. If serial register is mounted to IC2-IC4, the normal operation is not possible either.

CIRCUIT DIAGRAM

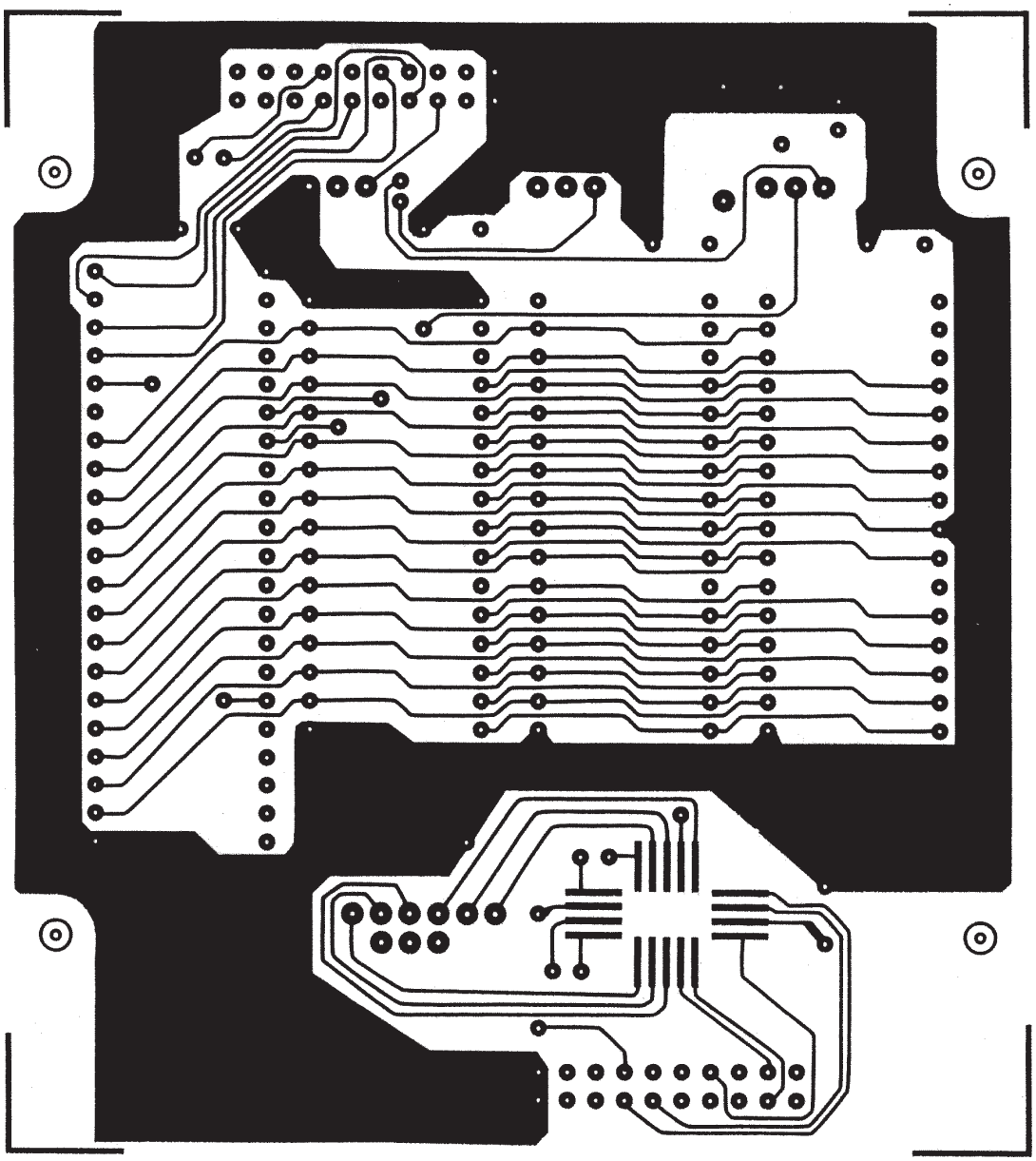


PATTERN LAYOUT

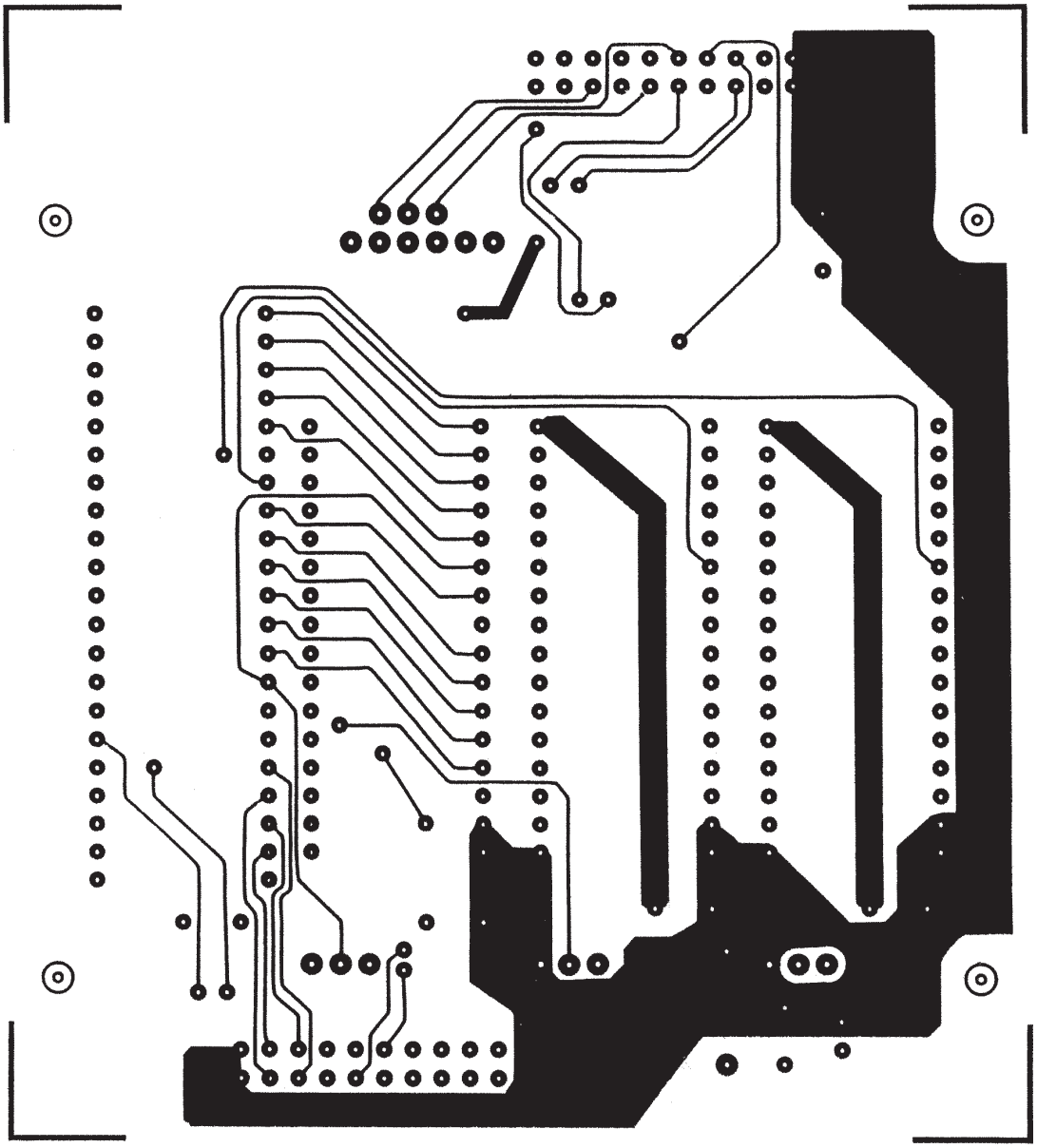
Silk Screen



Mounting Side



Solder Side



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