OKI Semiconductor

MSM6389C

1,048,576-Word x 1-Bit Solid-State Recorder Data Register

GENERAL DESCRIPTION

The MSM6389C is a solid-state recorder data register in 1,048,576 words x 1 bit configuration.

The MSM6389C has a built-in internal address generator circuit allowing continuous serial read/write operation by single external clock input. The internal address is automatically incremented by one by read/write operation.

Address designation in units of 1024 words in the direction of words is possible by an external serial address input.

The built-in refresh timer and refresh counter have eliminated the need of an external refresh circuit and realized a low power consumption.

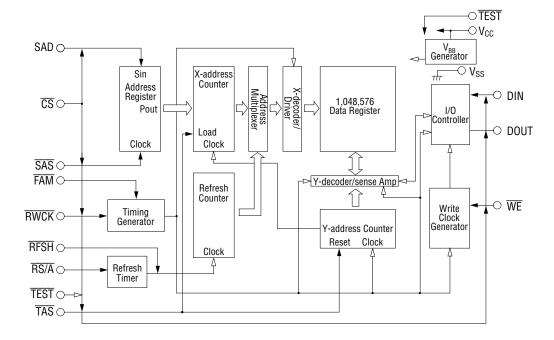18-pin plastic QFJ is used as the package and the operating temperature range is between 0°C and 70°C.

The MSM6389C is suitable for storing large capacity data with battery backup. A solid state recording and playback system can easily be constructed in combination with OKI's voice synthesizer ICs.
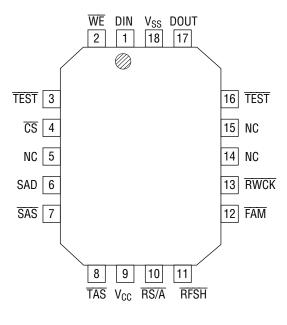
FEATURES

- Configuration: 1,048,576 x 1 bit
- Serial access operation:
    - Serial access time 1.5 µs (3.0 µs)
    - Serial read/write cycle time 2.0 µs (4.0 µs)
    - Fast mode read/write cycle time 0.4 µs (0.4 µs)
    - Times in parentheses indicate ones in self-refresh mode.
- Low current consumption: 100 µA max. (for data holding, V_{CC}=4.0V)
- Wide operating supply voltage range: Single 3.5 to 5.5V
- Auto-refresh/self-refresh changeable
- Package:
  18-pin plastic QFJ (QFJ18-P-R290-1.27) (Product name: MSM6389CJS)

BLOCK DIAGRAM

PIN CONFIGURATION (TOP VIEW)

NC: No connection

18-Pin Plastic QFJ

PIN DESCRIPTIONS

Pin	Symbol	Description
1	DIN	Data input
2	$\overline{\text{WE}}$	Write enable
3, 16	$\overline{\text{TEST}}$	Test input
4	$\overline{\text{CS}}$	Chip select
6	SAD	Serial address data
7	$\overline{\text{SAS}}$	Serial address strobe
8	$\overline{\text{TAS}}$	Transfer address strobe
9	V_{CC}	Power supply (+5V)
10	$\overline{\text{RS/A}}$	Self-refresh/auto-refresh select
11	$\overline{\text{RFSH}}$	Refresh clock input
12	$\overline{\text{FAM}}$	Fast access mode select
13	$\overline{\text{RWCK}}$	Read/write clock
17	DOUT	Data output
18	V_{SS}	Ground (0V)

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Condition	Rating	Unit
Terminal Voltage	V_T	T_a = 25°C, relative to V_{SS}	−1.0 to +7.0	V
Output Short-Circuit Current	I_{OS}	T_a = 25°C	50	mA
Power Dissipation	P_D	T_a = 25°C	1	W
Operating Temperature	T_{op}	—	0 to 70	°C
Storage Temperature	T_{STG}	—	−55 to +150	°C

RECOMMENDED OPERATING CONDITIONS

(T_a = 0 to 70°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	V_{CC}	3.5	4.5	5.5	V
Supply Voltage	V_{SS}	0	0	0	V
"H" Input Voltage	V_{IH}	V_{CC} − 0.5	V_{CC}	V_{CC} + 0.5	V
"L" Input Voltage	V_{IL}	−0.5	0	+0.5	V

ELECTRICAL CHARACTERISTICS

DC Characteristics

(V_{CC} = 3.5 V to 5.5 V, T_a = 0 to 70°C)

Parameter	Symbol	Condition	Min.	Max.	Unit
"H" Output Voltage	V_{OH}	I_{OH} = −0.5 mA	V_{CC} − 0.5	—	V
"L" Output Voltage	V_{OL}	I_{OL} = 0.5 mA	—	0.4	V
Input Leakage Current	I_{LI}	V_I = 0 V to V_{CC}	−1	+1	µA
Output Leakage Current	I_{LO}	V_O = 0 V to V_{CC}	−1	+1	µA
Supply Current (in operating state)	I_{CC1}	V_{CC} = 4 V, t_{RWC} = 2 µs	—	5	mA
Supply Current (in standby state)	I_{CC2}	V_{CC} = 4 V	—	100	µA
Supply Current (FAM)	I_{CC3}	V_{CC} = 4 V, t_{RWC} = 0.4 µs	—	15	mA

AC Characteristics

(V$_{CC}$ = 3.5 V to 5.5 V, Ta = 0 to 70°C)

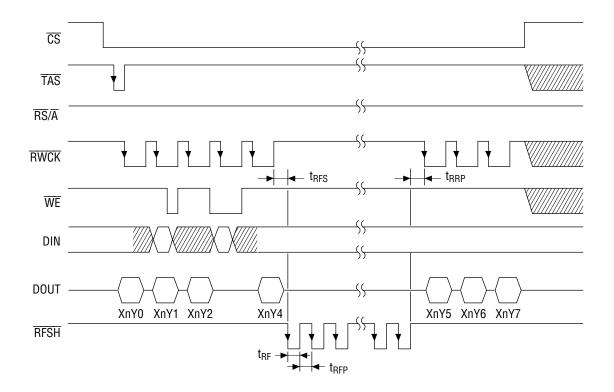| Parameter	Symbol	MSM6389C-SELF		MSM6389C-AUTO		Unit
		Min.	Max.	Min.	Max.	
Refresh Cycle	t$_{REF}$	—	—	—	100	ms
Read/Write Cycle Time	t$_{RWC}$	4,000	—	2,000	—	ns
Access Time	t$_{ACC}$	—	3,000	—	1,500	ns
Output Turn-off Delay Time	t$_{OFF}$	0	50	0	50	ns
Input Signal Rise/Fall Time	t$_T$	3	50	3	50	ns
$\overline{RWCK}$ Precharge Time	t$_{RWP}$	1,000	—	500	—	ns
$\overline{RWCK}$ Pulse Width	t$_{RW}$	3,000	10,000	1,500	10,000	ns
$\overline{SAS}$ Cycle Time	t$_{SSC}$	100	—	100	—	ns
$\overline{SAS}$ Pulse Width	t$_{SAS}$	50	—	50	—	ns
$\overline{SAS}$ Precharge Time	t$_{SAP}$	50	—	50	—	ns
Address Setup Time	t$_{AS}$	0	—	0	—	ns
Address Hold Time	t$_{AH}$	50	—	50	—	ns
$\overline{TAS}$ Setup Time	t$_{ATS}$	50	—	50	—	ns
$\overline{TAS}$ to $\overline{RWCK}$ Setup Time	t$_{TRS}$	50	—	50	—	ns
$\overline{TAS}$ Pulse Width	t$_{TAS}$	50	—	50	—	ns
Read Command Setup Time	t$_{RRS}$	0	—	0	—	ns
Read Command Hold Time	t$_{RRH}$	50	—	50	—	ns
Write Command Setup Time	t$_{WRS}$	0	—	0	—	ns
Write Command Hold Time	t$_{WRH}$	50	—	50	—	ns
Write Command Pulse Width	t$_{WP}$	50	—	50	—	ns
$\overline{WE}$ to $\overline{RWCK}$ Lead Tme	t$_{RWL}$	50	—	50	—	ns
Data Setup Time	t$_{DS}$	0	—	0	—	ns
Data Hold Time	t$_{DH}$	50	—	50	—	ns
$\overline{RWCK}$ to $\overline{WE}$ Delay Time	t$_{RWD}$	100	—	100	—	ns
$\overline{RFSH}$ Setup Time	t$_{RFS}$	—	—	500	—	ns
$\overline{RFSH}$ Precharge Time	t$_{RFP}$	—	—	500	—	ns
$\overline{RFSH}$ Pulse Width	t$_{RF}$	—	—	1,500	10,000	ns
$\overline{RFSH}$ $\overline{RWCK}$ Precharge Time	t$_{RRP}$	—	—	500	—	ns
Fast Mode Cycle Time	t$_{FC}$	400	—	400	—	ns
Fast $\overline{RWCK}$ Mode Access Time	t$_{FAC}$	—	300	—	300	ns
Fast $\overline{RWCK}$ Precharge Time	t$_{FCP}$	100	—	100	—	ns
Fast Mode $\overline{RWCK}$ Pulse Width	t$_{FR}$	300	—	300	—	ns
Fast Mode Setup Time	t$_{FS}$	0	—	0	—	ns
Fast Mode Hold Time	t$_{FH}$	50	—	50	—	ns
Fast Mode Width	t$_{FCC}$	4,000	100,000	2,000	100,000	ns
Slow Mode Setup Time	t$_{SS}$	0	—	0	—	ns
Slow Mode Hold Time	t$_{SH}$	50	—	50	—	ns

Note: Switching to the fast mode should be made satisfying the timings of t$_{FS}$ and t$_{SS}$ at the "L" level of $\overline{RWCK}$.
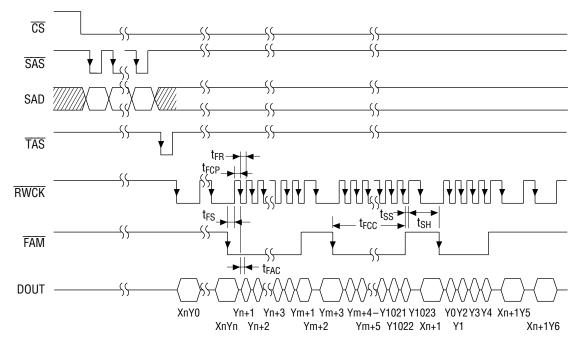
TIMING DIAGRAMS

Read/Write/Read Modify Write Cycle

Auto-Refresh Mode

Fast Access Mode

FUNCTIONAL DESCRIPTION

Serial Address Input (SAD)

Pin for inputting the start address for read/write. Address data can be input in units of 1024 words. The 1,024 address data can be input as 10-bit (A0-A9) serial from the SAD pin.

Serial Address Strobe ($\overline{\text{SAS}}$)

Pin for the clock used to store the serial address data into the internal register.

Address Transfer Strobe ($\overline{\text{TAS}}$)

Input pin for setting the serial address data stored in the address register to the internal address counter.

When the $\overline{\text{TAS}}$ falls, the Y address is set to address 0.

Read/Write Clock ($\overline{\text{RWCK}}$)

Input pin for the data register information read/write clock.

Internal operation starts at the falling edge of $\overline{\text{RWCK}}$. The information in the data register is output to the DOUT pin in the read mode, and the information at the DIN pin is written into the data register in the write mode. The internal address counter is automatically incremented when $\overline{\text{RWCK}}$ falls.

Write Enable ($\overline{\text{WE}}$)

Input pin for selecting the read mode, write mode or read modify write mode.

The read mode is set when $\overline{\text{WE}}$ is "H", and the write mode is set when $\overline{\text{WE}}$ is "L". When $\overline{\text{WE}}$ falls from "H" to "L" while $\overline{\text{RWCK}}$ is active, the read modify write mode is set.

Data Input (DIN)

Input pin for write data.

The information at the data input pin is stored at the falling edge of $\overline{\text{RWCK}}$ in the write mode, and at the falling edge of $\overline{\text{WE}}$ in the read modify write mode.

Data Output (DOUT)

The data output pin is always kept in the high impedance state when $\overline{\text{RWCK}}$ or $\overline{\text{CS}}$ is kept at "H". When "H" or "L" information is read in the read operation, the output pin is set to "H" to "L" and holds the read information until $\overline{\text{RWCK}}$ is again set to "H". In the early write mode the output pin maintains the high impedance state, so I/O common operation by connecting DIN and DOUT is possible.

Chip Select ($\overline{\text{CS}}$)

Input pin for disabling all input and output pins. This pin enables parallel use of multiple MSM6389Cs by connecting the data input and output pins.

Self/Auto Refresh Select ($\overline{\text{RS/A}}$)

Pin for selecting a refresh mode in order to retain memory cell data.

If the $\overline{\text{RS/A}}$ pin is set to "L" level, the self-refresh mode is selected and no external refresh control is required. If the $\overline{\text{RS/A}}$ pin is set to "H" level, the auto-refresh mode is selected and refresh operation is required to retain memory cell data.

Refresh Clock Input ($\overline{\text{RFSH}}$)

Input pin for controlling the external refresh when the auto refresh mode is selected. When the auto-refresh mode is selected, 1024 refresh operations are required within 100ms via the $\overline{\text{RFSH}}$ pin while the $\overline{\text{RWCK}}$ is at "H" level.

Fast Access Mode Select ($\overline{\text{FAM}}$)

Pin for fast read/write operations. Fast read/write is possible by keeping the $\overline{\text{FAM}}$ pin at "L" level. The fast access mode is set or released by inputting "L" level or "H" level to the $\overline{\text{FAM}}$ pin when the $\overline{\text{RWCK}}$ pin is at "L" level, and when t_{FS} and t_{SS} are satisfied.

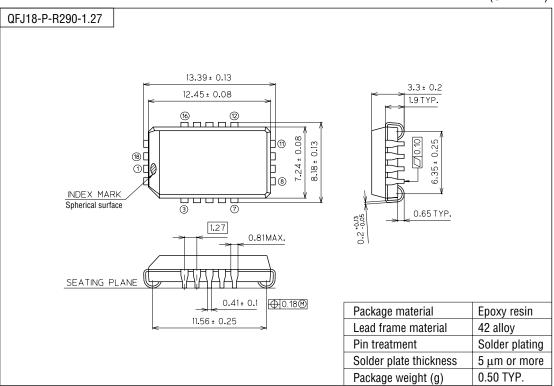When 1024-word data access is complete, be sure to insert a normal cycle in order to increment the X address.

Test ($\overline{\text{TEST}}$)

The $\overline{\text{TEST}}$ pin is fixed to "H" level.

Turning the power ON

To stabilize the device, it is required to pause for over 100μs after the V_{CC} reaches the specified voltage. Then it is needed to add eight or more $\overline{\text{RWCK}}$ cycles (read cycles or pseudo data write cycles).

PACKAGE DIMENSIONS

(Unit : mm)

QFJ18-P-R290-1.27

13.39 ± 0.13
12.45 ± 0.08

16   12

18

1

INDEX MARK
Spherical surface

11

8

7.24 ± 0.08
8.18 ± 0.13

3   7

3.3 ± 0.2
1.9 TYP.

0.10

6.35 ± 0.25

0.65 TYP.

0.2 +0.13 −0.05

1.27   0.81MAX.

SEATING PLANE

0.41 ± 0.1   0.18 Ⓜ

11.56 ± 0.25

Package material	Epoxy resin
Lead frame material	42 alloy
Pin treatment	Solder plating
Solder plate thickness	5 μm or more
Package weight (g)	0.50 TYP.

Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, TQFP, LQFP, SOJ, QFJ (PLCC), SHP, and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact Oki's responsible sales person on the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

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