



### Typical Applications

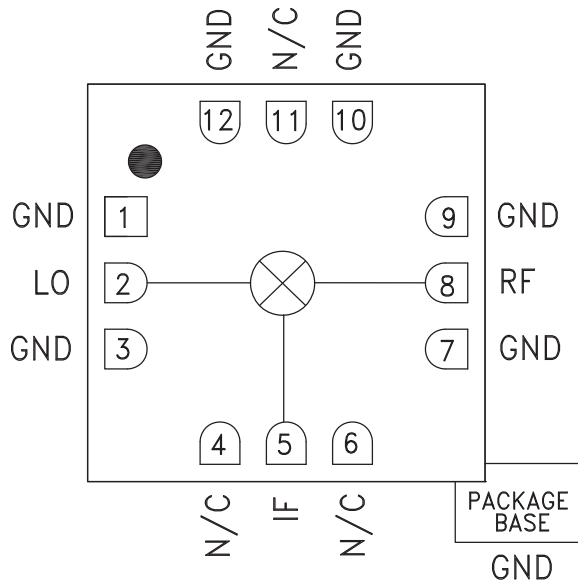
The HMC774LC3B is ideal for:

- Point-to-Point Radios
- Point-to-Multi-Point Radios & VSAT
- Test Equipment & Sensors
- Military End-Use

### Features

- Passive: No DC Bias Required
- Input IP3: +22 dBm
- LO/RF Isolation: 35 dB
- Wide IF Bandwidth: DC - 8 GHz
- 12 Lead Ceramic 3x3 mm SMT Package: 9mm<sup>2</sup>

### Functional Diagram



### General Description

The HMC774LC3B is a general purpose double balanced mixer in a leadless RoHS compliant SMT package that can be used as an upconverter or downconverter between 7 and 34 GHz. This mixer requires no external components or matching circuitry. The HMC774LC3B provides excellent LO to RF and LO to IF suppression due to optimized balun structures. The mixer operates best with LO drive levels above +15 dBm. The HMC774LC3B eliminates the need for wire bonding, allowing use of surface mount manufacturing techniques.

### Electrical Specifications, $T_A = +25^\circ\text{C}$ , IF = 0.5 GHz, LO = +15 dBm\*

Parameter	Min.	Typ.	Max.	Min.	Typ.	Max.	Units
Frequency Range, RF & LO	7 - 20		20 - 34				GHz
Frequency Range, IF	DC - 8		DC - 8				GHz
Conversion Loss		10	13		11	14	dB
LO to RF Isolation		35			35		dB
LO to IF Isolation	20	30		25	40		dB
RF to IF Isolation	7	10		14	20		dB
IP3 (Input)		20			22		dBm
IP2 (Input)		45			48		dBm
1 dB Gain Compression (Input)		12			13		dBm

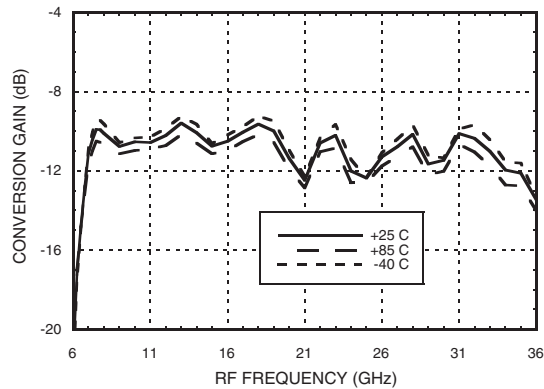
\*Unless otherwise noted, all measurements performed as downconverter, IF = 0.5 GHz.



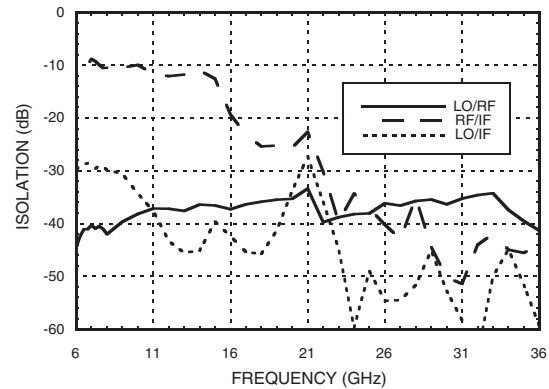
# HMC774LC3B

## GaAs MMIC FUNDAMENTAL MIXER, 7 - 34 GHz

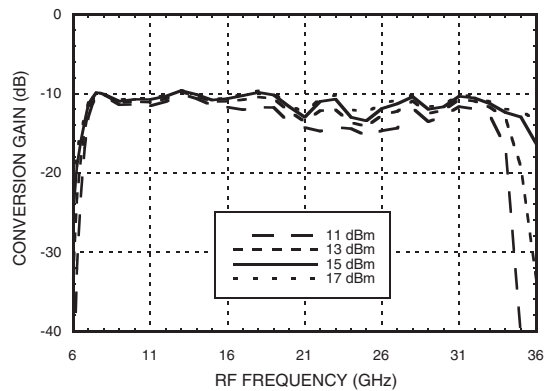
**Conversion Gain vs. Temperature**



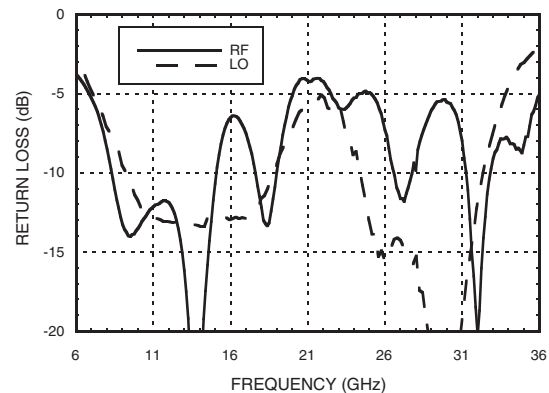
**Isolation**



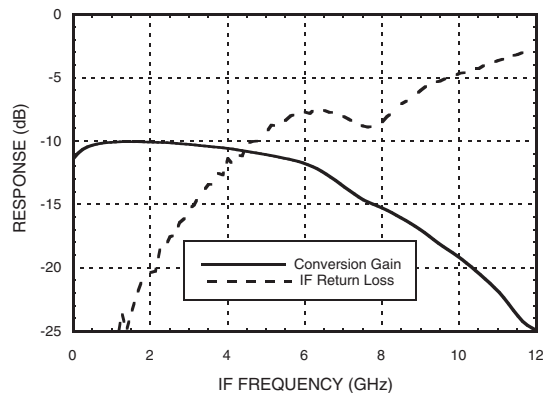
**Conversion Gain vs. LO Drive**



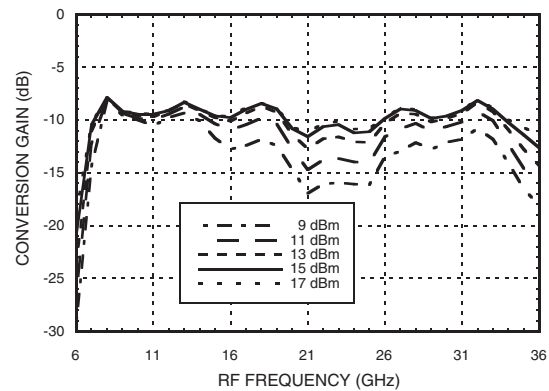
**Return Loss**



**IF Bandwidth**



**Upconverter Performance  
Conversion Gain vs. LO Drive**

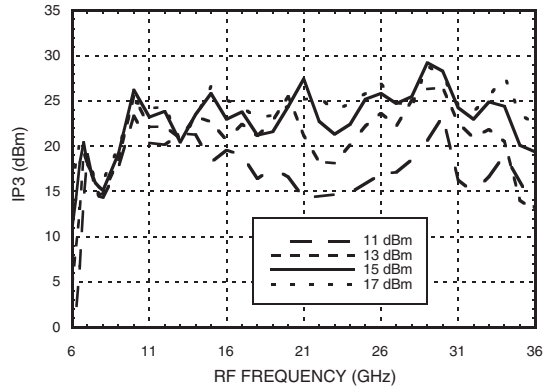




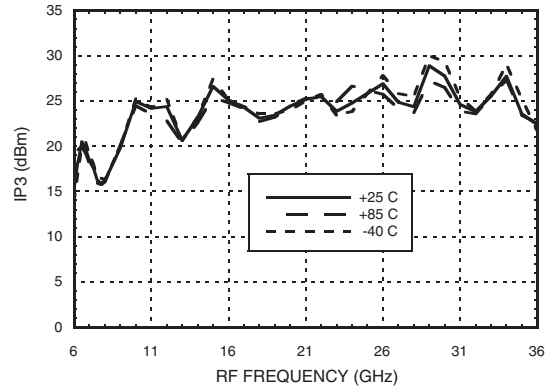
## HMC774LC3B

### GaAs MMIC FUNDAMENTAL MIXER, 7 - 34 GHz

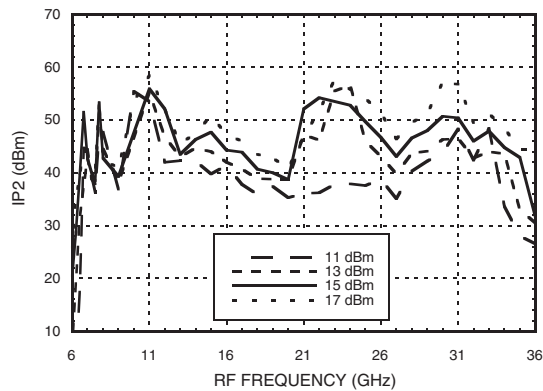
**Input IP3 vs. LO Drive [1]**



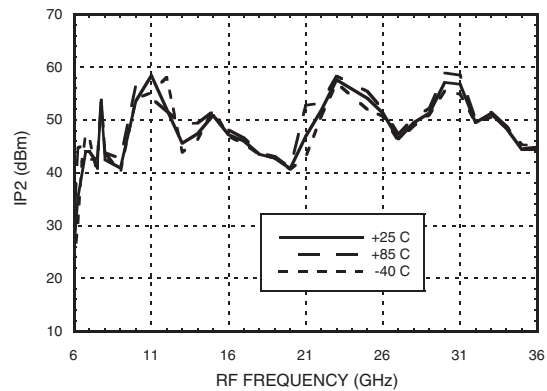
**Input IP3 vs. Temperature [1]**



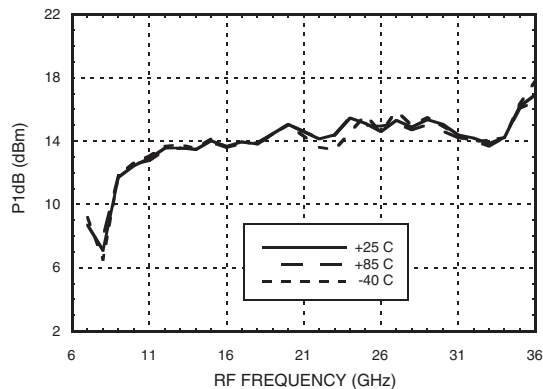
**Input IP2 vs. LO Drive [1]**



**Input IP2 vs. Temperature [1]**



**Input P1dB vs. Temperature**



**MxN Spurious Outputs**

mRF	nLO				
	0	1	2	3	4
0	xx	10	39	xx	xx
1	5	0	37	43	xx
2	30	49	47	55	68
3	xx	74	62	45	63
4	xx	xx	xx	77	71

RF = 17.5 GHz @ -10 dBm  
 LO = 18 GHz @ +15 dBm  
 All values in dBc below the IF output power level.

[1] Two-tone input power = -10 dBm each tone, 1 MHz spacing.



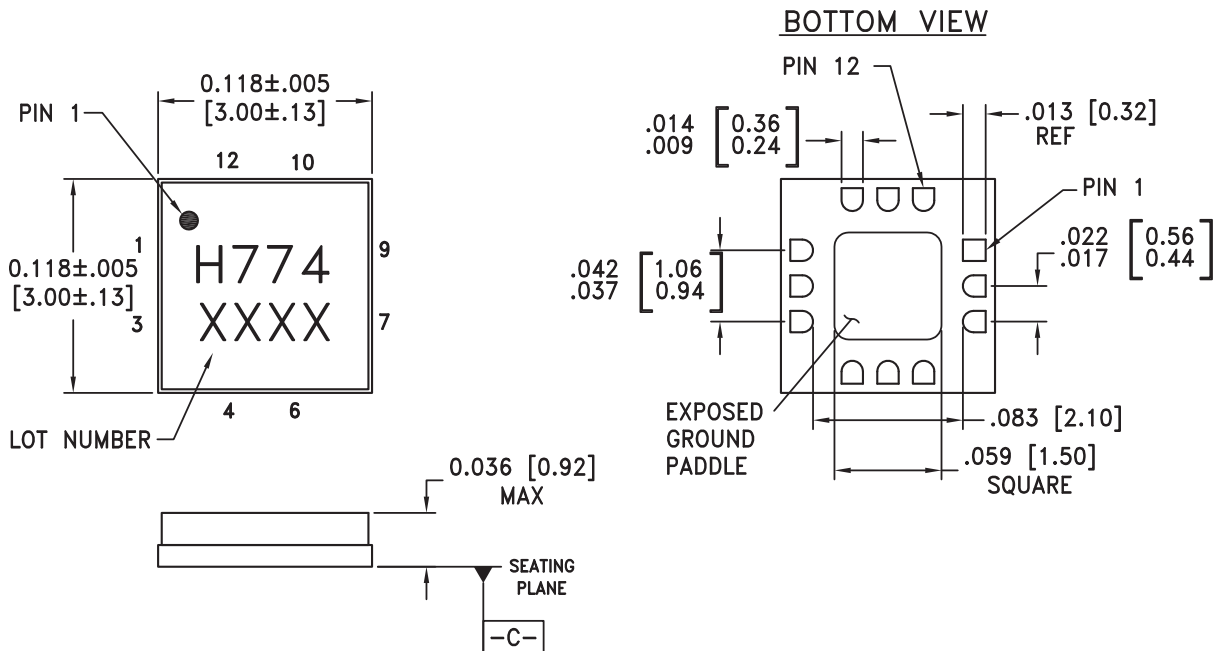
**Absolute Maximum Ratings**

RF / IF Input	+21 dBm
LO Drive	+27 dBm
Channel Temperature	150 °C
Continuous Pdiss (Ta = 85 °C) (derate 2.9 mW/°C above 85 °C)	189 mW
Thermal Resistance (channel to ground paddle)	343 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C



ELECTROSTATIC SENSITIVE DEVICE  
OBSERVE HANDLING PRECAUTIONS

**Outline Drawing**


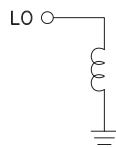
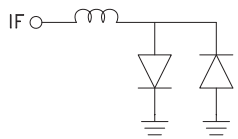
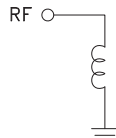


NOTES:

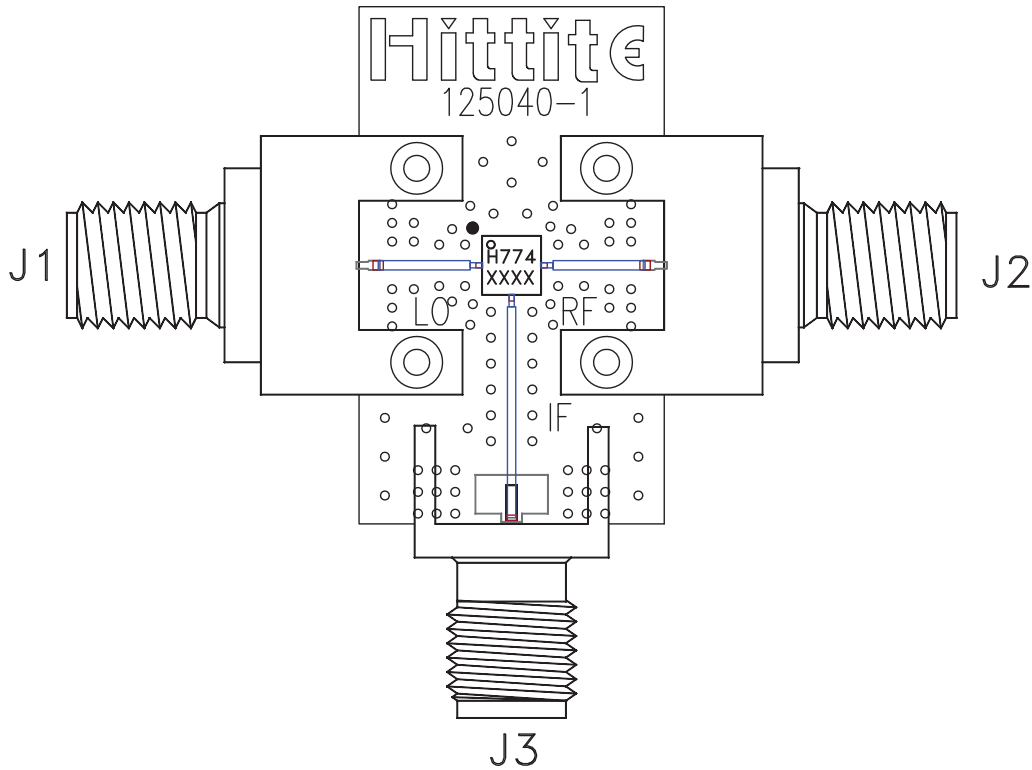
1. PACKAGE BODY MATERIAL: ALUMINA.
2. LEAD AND GROUND PADDLE PLATING: GOLD FLASH OVER NICKEL.
3. DIMENSIONS ARE IN INCHES (MILLIMETERS).
4. LEAD SPACING TOLERANCE IS NON-CUMULATIVE.
5. CHARACTERS TO BE HELVETICA MEDIUM, .025 HIGH, BLACK INK, OR LASER MARK LOCATED APPROX. AS SHOWN.
6. PACKAGE WARP SHALL NOT EXCEED 0.05MM DATUM - C -
7. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
8. CLASSIFIED AS MOISTURE SENSITIVITY LEVEL (MSL) 1.



**Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1, 3, 7, 9, 10, 12	GND	These pins and package bottom must be connected to RF/DC ground.	
2	LO	This pin is DC coupled and matched to 50 Ohms.	
4, 6, 11	N/C	These pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	
5	IF	This pin is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pin must not source or sink more than 2 mA of current or part non-function and possible part failure will result.	
8	RF	This pin is DC coupled and matched to 50 Ohms.	

**Evaluation PCB**



**List of Materials for Evaluation PCB 125042 [1]**

Item	Description
J1 - J2	Southwest K Connector
J3	Johnson SMA Connector
U1	HMC774LC3B Mixer
PCB [2]	125040 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Arlon 25FR or Rogers 4350

The circuit board used in this application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.